



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/627,769	07/28/2003	Masatoshi Hasegawa	501.39835CX1	4302
20457	7590	09/08/2004	EXAMINER	
ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800 ARLINGTON, VA 22209-9889				YOHA, CONNIE C
		ART UNIT		PAPER NUMBER
		2818		

DATE MAILED: 09/08/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

P

Office Action Summary	Application No.	Applicant(s)
	10/627,769	HASEGAWA ET AL.
	Examiner	Art Unit
	Connie C. Yoha	2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 23 December 2003.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1,2,14-19 and 29-33 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) 14-19 and 29-33 is/are allowed.

6) Claim(s) 1 and 2 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 28 July 2003 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____ .
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>7/03</u> .	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

1. This office acknowledges receipt of the following items from the Applicant:
Papers submitted under 35 U.S.C. 119(a)-(d) have been placed of record in the file.

Information Disclosure Statement (IDS) filed on 7/28/03 was considered.
2. Claims 1-33 are presented for examination.
3. Claims 3-13, and 20-28 are canceled.
4. Claims 1, 2, 14-19, and 29-33 are pending.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1-2 are rejected under 35 U.S.C. 102(b) as being anticipated by Hidaka, Pat. No. 6233181.

With regard to claim 1, Hidaka discloses a memory device comprising: a plurality of memory mats (fig. 1, MB#00-MB#mn) arranged in a bit-line direction which includes bit lines, word lines and memory cells coupled to the bit lines and the wordlines (col. 10, line 28-39); a MOSFET included in each of the memory cells and having a capacitance having first and second electrodes, a gate coupled to a corresponding one of the wordlines and source-and-drain paths, one of which is coupled to a corresponding one

of the bitlines and the other of which is coupled to the first electrode of the capacitance (fig. 16, Mca); and a sense amplifier array (fig. 11, SAB0-SABm) provided in a region between the memory mats arranged in a direction of the bitline and having latch circuits having input/output nodes connected to half of the bit line pairs provided to each of the memory mats (fig. 14 and 15), wherein a failed bitline of the bitline pairs can be replaced, on a bitline basis, with a redundant bitline and a corresponding redundant sense amplifier (col. 10, line 40-48)(col. 11, line 24-col 12, line 10).

With regard to claim 2, Hidaka discloses wherein said failed bitline is failed due to the presence of a failure on the memory cell itself (col. 1, line 16-24).

Allowable Subject Matter

6. Claim 14-19, 29-33 are allowed.

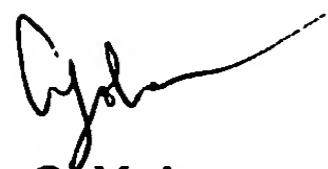
Claims 14-19, 29-33 are considered allowable since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. The prior art does not teach the claimed invention having in combination with other features, wherein said first normal bit line is to be replaced with said first redundant bit line but said second normal bit line is not to be replaced with said second redundant bit line.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicants' disclosure. Yoshitake et al (5808944) disclose a memory device having defect relief arrangement.
8. When responding to the office action, Applicants' are advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist the examiner to locate the appropriate paragraphs.
9. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) day from the date of this letter. Failure to respond within the period for response will cause the application to become abandoned (see MPEP 710.02 (b)).
10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to whose telephone number is (571) 272-1799. The examiner can normally be reached on Mon. - Fri. from 8:00 A.M. to 5:30 PM. The examiner's supervisor, David Nelms, can be reached at (571) 272-1787. The fax phone number for this Group is (703) 872-9306. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-0956.
11. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov> should you

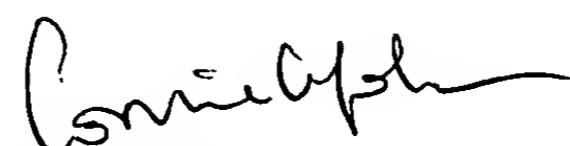
Art Unit: 2818

have questions on access to the Private Pair system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



C. Yoha

May 2004



Connie C. Yoha

Art Unit 2818